

Electronic Acknowledgement Receipt

EFS ID:	3730940
Application Number:	10534962
International Application Number:	
Confirmation Number:	1696
Title of Invention:	Circuit board, multi-layer wiring boards, method of producing circuit boards and method of producing multilayer wiring boards
First Named Inventor/Applicant Name:	Masayoshi Kondo
Customer Number:	25461
Filer:	Robert G. Weillacher/Susan Revell
Filer Authorized By:	Robert G. Weillacher
Attorney Docket Number:	033036.088
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Submitted with Payment	yes
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Payment was successfully received in RAM	\$ 120
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1		20080805022136.PDF	554507 483b61f431e1d034c4c3dde6f5c3ea4 4be3bcac	yes	14
Multipart Description/PDF files in .zip description					
		Document Description	Start	End	
		Extension of Time	1	1	
		Amendment After Final	2	14	
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8208 0485b46101a1b022ad4f204a9c2cb84ad dfdc5c45	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			562715		
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